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Example2: Why cant use blocking statements in a sequential blocks

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VHDL and Verilog are HDL and is used for RTL coding to produce synthesizable digital models. Although there could be wide range of questions and can be asked from any topics, so it's always better to grasp all the concepts and also the level of tough...

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Example: "Verilog is syntactically similar to a C type programming language while VHDL is more similar to the ADA language. Verilog is easy to learn and simple to write, but VHDL takes a longer time to learn and requires more complex written code. Verilog also supports lower-level logic representation, whereas VHDL does not."

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No explanation is available for this question! 6) In VHDL, which class of scalar data type represents the values

necessary for a specific operation? a. Integer types b. Real types c. Physical types d. ... Among the VHDL features, which language statements are executed at the same time in parallel flow? a. Concurrent b. Sequential c. Net-list d.

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Reference Designer

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