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Hardware-Software Co-Design for General-Purpose Processors [1/14] Hardware-Software Co-Design

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#### Hardware Software Co Design Proceedings

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#### Hardware/Software Co-Design - Proceedings of the IEEE

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Hardware/software co-design means meeting system level objectives by exploiting the synergism of hardware and software through their concurrent design. Co-design problems have different flavors according to the application domain, implementation technology and design methodology. Digital hardware design has increasingly more similarities to software design.

#### Hardware/software co-design - IEEE Journals & Magazine

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#### Hardware/software co-design (panel) | Proceedings of the ...

The paper presents the parallelizing programming environment CoDe-X introducing hardware/software co-design strategies on two levels of partitioning for data-driven Xputer-based accelerators. CoDe-X performs both, in the first level a profiling-driven host/accelerator partitioning for performance optimization, and in a second level a resource-driven sequential/structural partitioning of the ...

#### Hardware/Software Co-Design for Data-Driven Xputer-based ...

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#### HARDWARE-SOFTWARE CO-DESIGN FOR HIGH ... - proceedings.com

Hardware/Software Co-Design by Asawaree Kalavade and Edward A. Lee Proceedings of the IFIP International Workshop on Hardware/Software Co-Design Grassau, Germany May 19-21, 1992 Appeared In: Proceedings of the First Intl. Workshop on Hardware/Software Codesign, Color ado, Sept. 1992. ABSTRACT

#### Hardware/Software Co-Design - Ptolemy Project

SPIE Digital Library Proceedings. 3 May 2011 Hardware-software-co-design of parallel and distributed systems using a behavioural programming and multi-process model with high-level synthesis

#### Hardware-software-co-design of parallel and distributed ...

ABSTRACT. We present a library in Haskell for programming Field Programmable Gate Arrays (FPGAs), including hardware software co-design. Code for software (in C) and hardware (in VHDL) is generated from a single program, along with the code to support communication between hardware and software. We present type-based techniques for the simultaneous implementation of more than one embedded domain specific language (EDSL).

#### Hardware software co-design in Haskell | Proceedings of ...

Hardware/software codesign and system synthesis, International conference on. Sept. 8 2004 to Sept. 10 2004. Stockholm, Sweden. ISBN: 1-58113-937-3

#### Hardware/software codesign and system synthesis ...

Bolsens, I.; Hugo J. de Man et al: Hardware/Software Co-Design of Digital Telecommunication Systems. Proceedings of the IEEE, Vol. 85, No. 3, March 1997 Google Scholar [7.5]

#### Hardware/Software Co-Design | SpringerLink

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hardware-software co-design are critical research topics for embedded system design. Hardware-software co-design of embedded systems must be performed at several different levels of abstraction, but the highest levels of abstraction in co-design are more abstract than the typical software coder or ASIC designer may be used to.

#### Hardware-software co-design of embedded systems ...

CODES/ISSS '19: Proceedings of the International Conference on Hardware/Software Codesign and System Synthesis Companion

#### Proceedings of the International Conference on Hardware ...

Hardware/Software Co-Design: Proceedings of the NATO Advanced Study Institute, Tremezzo, Italy, June 19-30, 1995: Micheli, G.De, Sami, Mariagiovanna: Amazon.sg: Books

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To achieve this goal, we propose GraphDynS, a hardware/software co-design with decoupled datapath and data-aware dynamic scheduling. Aware of data dependencies extracted from the decoupled datapath, GraphDynS can elaborately schedule the program on-the-fly to maximize parallelism.

#### Alleviating Irregularity in Graph Analytics Acceleration ...

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#### (PDF) Hardware/Software Co-Design - ResearchGate

Languages". Proceedings of the 26th 2014 International Symposium on Implementation and Application of Functional Languages. ACM. 2014, p. 8 Paper B Markus Aronsson and Mary Sheeran. \Hardware Software Co-Design in Haskell". Proceedings of the 10th ACM SIGPLAN International Sympo-sium on Haskell. ACM. 2017, pp. 162{173

#### A Functional Approach to Hardware Software Co-Design.

Aspects of systems architecture in hardware/software co-design. In E. Lee, G. de Micheli, W. Rosenstiel, & L. Thiele (Eds.), Design Automation for Embedded Systems (pp. 22). Schloss Dagstuhl -

Leibniz Zentrum für Informatik. Dagstuhl Seminar Proceedings, Vol.. 143