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Altera SDK for OpenCL Getting Started Guide 1 2014.12.15 OCL001-14.1.0 Subscribe Send Feedback The Altera SDK for OpenCL Getting Started Guide describes the procedures you follow to install the Altera® Software Development Kit (SDK) for OpenCL™ (AOCL)(1).This document also contains instructions on how to compile an example OpenCL(2) application with the AOCL.

Altera SDK for OpenCL - Intel

Prerequisites for the Altera SDK for OpenCL To install the Altera SDK for OpenCL and create an OpenCL application for an Altera preferred accelerator board, your system must meet certain hardware, target platform, and software requirements. (1) The Altera SDK for OpenCL is based on a published Khronos Specification, and has passed the Khronos

Altera SDK for OpenCL - Intel

The Altera SDK for OpenCL (AOCL) host can execute a kernel as a single work-item, which is equivalent to launching a kernel with an NDRange size of (1, 1, 1). The OpenCL Specification version

1.0 describes this mode of operation as task parallel programming.

Altera SDK for OpenCL - Intel

Altera SDK for OpenCL version 14.0. Related Links Custom Platform Automigration for Forward Compatibility 1.2 Features of the Stratix V Network Reference Platform Prior to designing an Intel FPGA SDK for OpenCL Custom Platform, decide on design considerations that allow you to fully utilize the available hardware on your computing card. Figure 1.

Intel FPGA SDK for OpenCL

Contents of the Altera SDK for OpenCL version 13.0 SP1 The Altera SDK for OpenCL version 13.0 SP1 provides the following logical components: Compiler—The compiler translates your OpenCL C device code into a hardware configuration file that can be loaded onto an Altera FPGA. Altera SDK for OpenCL (AOCL) Utility —The AOCL utility includes a set of

Altera SDK for OpenCL Programming Guide - intel.cn

set up and use the Altera® Software Development Kit (SDK) for OpenCL™ (AOCL) to run an OpenCL application on the Altera Cyclone® V SoC Development Kit. OpenCL (1) is a C-based open standard for the programming of heterogeneous parallel devices.

Altera SDK for OpenCL - CiteSeerX

OpenCL License Bundle. Combining the Open Computing Language (OpenCL™) programming model with Altera's massively parallel FPGA architecture provides a powerful solution for system acceleration. The Altera® SDK for OpenCL* provides a design environment for you to easily implement OpenCL applications on FPGAs. Altera's Quartus® II software is the industry's number one software in productivity and performance for CPLD, FPGA, and SoC designs.

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Intel® SDK for OpenCL™ Applications

The Intel FPGA SDK for OpenCL provides you an environment to target FPGAs while abstracting FPGA details. (1) OpenCL and the OpenCL logo are trademarks of Apple Inc. used by permission of the Khronos Group™. (2) The Intel FPGA SDK for OpenCL is based on a published Khronos specification, and has passed the Khronos Conformance Testing Process.

Intel FPGA SDK for OpenCL

The Altera SDK for OpenCL enables programmers to leverage the

massively parallel, fine-grained architectures featured in FPGAs to accelerate parallel computation. Unlike CPUs and GPGPUs, where parallel threads are executed across an array of cores, FPGAs allow kernel functions to be transformed into dedicated, deeply pipelined hardware circuits that are multithreaded using the concept of ...

Electro-Magnetic World: Altera SDK for OpenCL

1. Download the .tar file. 2. Extract the files into temporary directory. 3. Run the setup.sh file to install Intel FPGA SDK for OpenCL with Quartus Prime Standard Edition. 4. Run the setup_pro.sh file to install Intel FPGA SDK for OpenCL with Quartus Prime Pro Edition.

Download Center for FPGAs

Optimization Training OpenCL™ Coding Optimizations for Intel® Stratix® 10 Devices (23 minutes). In this course, we will cover how the offline kernel compiler of the Intel® FPGA SDK for OpenCL™ optimizes OpenCL kernel code for optimal performance on Intel® Stratix® 10 FPGAs and how to use recommended coding constructs to enable these optimizations.

Intel® FPGA SDK for OpenCL™ - Intel FPGA SDK for OpenCL

Intel® FPGA SDK for OpenCL™ software technology 1 is a world class development environment that enables software developers to accelerate their applications by targeting heterogeneous platforms with Intel CPUs and FPGAs.

Intel® FPGA SDK for OpenCL™ Software Technology

The Altera® SDK for OpenCL provides a design environment enabling users to easily implement OpenCL applications on Altera's FPGAs. Altera Quartus II The OpenCL Developer's Bundle includes the Altera Quartus II software, the complete software development package for the Altera Arria 10 and Stratix V FPGAs.

OpenCL Developer's Bundle - BittWare FPGA Acceleration

The SDK has now been made public. Altera's implementation is built on top of OpenCL 1.0 but offers custom extensions to tap into the unique features of FPGAs. More information can be found on...

A Look at Altera's OpenCL SDK for FPGAs - AnandTech

Querying platform info on my host code I get: OpenCL 1.0 Altera SDK for OpenCL, Version 15.0 . I have a few questions and problems that I would be extremely grateful if someone can help me with. 1) Using the emulator, I timed my kernels for the FPGA.

SLAM on FPGA using Altera OpenCL - Intel Community

SAN JOSE, Calif., June 30, 2014 /PRNewswire/ -- Altera Corporation today dramatically simplified a programmer's ability to accelerate algorithms in FPGAs. The Altera SDK for OpenCL version 14.0... | October 15, 2020